Washington, D.C. 20231



## **PATENT**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:	)		
Norman C. Chou, et al.	)	Examiner:	Not Yet Assigned
Application No.: 10/057,159	) )	Art Unit:	2131
Filed: January 24, 2002	)		RECEIVED
For: CONTROL OF AUTHENTICATION DATA RESIDING IN A NETWORK DEVICE	) ) <u>)</u>		Technology Center 2100
Assistant Commissioner for Patents			2100

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Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents,
Washington, D.C. 20231 on <u>September 30, 2002</u>
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Pursuant to 37 C.F.R. § 1.97, the submission of this Information

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Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: <u>09/36</u>, 2002

André L. Marais Reg. No. 48,095

12400 Wilshire Blvd. Seventh Floor Los Angeles, CA 90025-1026 (408) 720-8300

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Substitute for Form 1449A/PTO (Modified)

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Of

Complete if Known			
Application Number	10/057,159		
Filing Date	January 24, 2002		
First Named Inventor:	Norman C. Chou, et a	ab	
Group Art Unit	2131	HECE	
Examiner Name	Not Yet Assigned	00-	
Attorney Docket Number	5288.P017	00/092	

		OTHER ART – NO PATENT LITERATURE DOCUMENTS  Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the
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A B		"InfiniBand Switch Chip Runs at 10 Gbps On Eight Ports", Nicholas Cravotta, 11/8/2001, EDN, 1 page
		"Assemble Fast Switch Fabrics With 32-Port InfiniBand Node p. 60", Electronic Design, 10/15/2001, 4 pages
•		"RedSwitch, Inc. Announces Industry's Highest Performance and Highest Integration InfiniBand Switch Chip", RedSwitch Press Release, 10/16/2001, 2 pages.
		"RedSwitch Gearing Up To Launch New Chip", Steve Tanner, Silicon Valley Business Ink,10/26/2001, 3 pages.
		"Mellanox Integrates Serdes Into Infiniband Switch", Jerry Ascierto, EE Times, 10/23/2001, 3 pages.
		"Switch Chip Expands InfiniBand Integration", EEM File 3130, Tony Chance, 2 pages.
		"RedSwitch Announces 16 Gbyte/s Throughout Switch Product for RapidIO Architecture", RedSwitch Press Release, Milpitas, Calif., May 15, 2001, Tony Chance,5/15/2001, 2 pages.
		"RedSwitch and Agilent Technologies Unveil 160-GB/s Throughout Switch Product for InfiniBand Architecture", RedSwitch Press Release, Intel Developer Forum Conference, San Jose, Calif., Feb. 27, 2001, Mark Alden-Agilent, Tony Chance-RedSwitch, 2 pages.

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